

17 signal output (OUT), and
18 the output of the window discriminator is linked to
19 the input of a logic gate (L1, L2),
20 the output of the logic gate (L1, L2) is linked to the
21 control input of the switching element (E1, E2), and
22 the output of at least one logic gate (L1, L2) is also
23 linked to the circuit unit (SCH1) for the selection of
24 defined circuit elements within the circuit unit (SCH1).

REMARKS:

- 1) Examination of the present application is to proceed on the basis of claims 1 to 13, 16 and 17. Claims 16 and 17 are directly based on original claims 14 and 15 respectively, but omit the original multiple dependencies.
- 2) Favorable consideration and allowance of claim 1 to 13, 16 and 17 are respectfully requested.

Respectfully submitted,

Matthias EICHIN et al.
Applicant

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Encls.: postcard

By Walter F. Fasse
Walter F. Fasse
Patent Attorney
Reg. No.: 36132
Tel. No.: (207) 862-4671
Fax. No.: (207) 862-4681
P. O. Box 726
Hampden, ME 04444-0726

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